

Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

U.S. UTILITY Patent Application

SCANNED	O.I.P.E.	PATENT DATE
MD	TD4	Q.A. SW

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/838610		7.14	784	2133	Decadyr torres

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TITLE

Code-programmable field-programmable architecturally-systolic Reed-Solomon BCH error correction decoder integrated circuit and error correction decoding method

PTO-2040  
12/89

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>  <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____.  <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<b>DRAWINGS</b> Sheets Drwg.    Figs. Drwg.    Print Fig.			<b>CLAIMS ALLOWED</b> Total Claims    Print Claim for O.G.	
	(Assistant Examiner)    (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
	(Primary Examiner)    (Date)			<b>ISSUE FEE</b> Amount Due    Date Paid	
	(Legal Instruments Examiner)    (Date)			<b>ISSUE BATCH NUMBER</b>	

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